

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	922	(absolute near3 address) with (relative near3 address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:45
L2	6162	(710/8,22,52.ccls. or 711/147, 148,152,153.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:39
L3	1752	L2 and (((first and second) multipl\$5 plural\$4) near3 (processor processing)) and ((plural\$4 multiple) with (memory buffer banks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:39
L4	8	L3 and I1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:40
L5	1517	(absolute near3 address) and (relative near3 address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:45
L6	15	3 and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/14 15:45
S1	6160	(710/8,22,52.ccls. or 711/147, 148,152,153.ccls.)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 08:30
S2	1751	S1 and (((first and second) multipl\$5 plural\$4) near3 (processor processing)) and ((plural\$4 multiple) with (memory buffer banks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:30
S3	318	S2 and ((allocat\$4 assign\$4 partition) with (memory buffer bank) with (application process program task))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:45

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S4	99	S3 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:45
S5	55523	(((first and second) multipl\$5 plural\$4) near3 (processor processing)) and ((plural\$4 multiple) with (memory buffer banks))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:44
S6	12641	(((first and second) multipl\$5 plural\$4) near3 (processor processing CPU)) and ((plural\$4 multiple) with (memory buffer banks)) and (external near3 (processor processing device CPU))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:45
S7	1400	S6 and ((allocat\$4 assign\$4 partition) with (memory buffer bank) with (application process program task))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:45
S8	549	S7 and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:45
S9	482	S8 and (@ad<="20020807" or @rlad<="20020807")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:22
S10	16534	S9 (internal near3 (memory buffer bank)) and (external near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:47
S11	291	S9 and (internal near3 (memory buffer bank)) and (external near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 09:52
S12	124	(allocat\$4 assign\$4) with (plural\$4 multipl\$4) with (memory buffer banks module) with (processor processing CPU) with external	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:11

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S13	701115	((first and second) multipl\$4 plural\$4) with (memory buffer storage bank)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:12
S14	91640	((first and second) multipl\$4 plural\$4) and external) with (memory buffer storage bank)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S15	721650	((first and second) multipl\$4 plural\$4) with (processing processor CPU controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:16
S16	86955	((first and second) multipl\$4 plural\$4) and external) with (processing processor CPU controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S17	215040	(allocat\$4 assign\$4 designat\$4) with (memory buffer storage rank)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:17
S18	29074	(allocat\$4 assign\$4 designat\$4) with (memory buffer storage rank) with (application program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S19	3315	S14 and S16 and S18	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:18
S20	36487	((first and second) multipl\$4 plural\$4) and external) near3 (memory buffer storage bank)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S21	24992	((first and second) multipl\$4 plural\$4) and external) near3 (processing processor CPU controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S22	6898	(allocat\$4 assign\$4 designat\$4) near3 (memory buffer storage rank) near3 (application program)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21

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S23	197	S20 and S21 and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:21
S24	169	S23 and (@ad<="20020807" or @rlad<="20020807")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/10 13:22
S25	2143	(memory near3 interface) with DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 13:43
S26	52	(internal and external) with (memory near3 interface) with DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 13:44
S27	463	(DMA with (memory near3 module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 13:54
S28	90	(DMA with interface with (memory near3 module))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:10
S29	1	WMIF and FMIF and CPUIF	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:10
S30	49	(first near3 memory near3 interface) same (second near3 memory near3 interface) same (third near3 memory near3 interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:14
S31	4	(first near3 memory near3 interface) same (second near3 memory near3 interface) same (third near3 memory near3 interface) same DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:17
S32	7	(first near3 memory near3 interface) same (second near3 memory near3 interface) same (third near3 memory near3 interface) and DMA	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:23

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S33	86	(first and second and third) near3 (DMA near3 controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 14:24
S34	2	"6057862".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 16:11
S35	3279	(register with capacity with (memory storage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 16:11
S36	447	(register with (set\$4 determin\$4) with capacity with (memory storage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 16:11
S37	546	(register with (set\$4 determin\$4 specif\$5) with capacity with (memory storage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 16:12
S38	70	(register near3 (set\$4 determin\$4 specif\$5) near3 capacity near3 (memory storage))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/13 16:12